

Docket No.: 295586US-2 PCT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION: Genichi FUJIWARA

SERIAL NO.: 10/593,713

GAU:

FILED: September 21, 2006

EXAMINER:

FOR: PHASE LOCKED LOOP (PLL) CIRCUIT, ITS PHASING METHOD AND OPERATION
ANALYZING METHOD

LETTER SUBMITTING REPLACEMENT DRAWING SHEET(S)

COMMISSIONER FOR PATENTS
Alexandria, VA 22313

SIR:

Responsive to the below indicated communication, the following drawing sheets are submitted herewith:

☒ 1 Replacement Drawing Sheet

☐ _____ New Drawing Sheets

☐ Official Action dated _____

☐ Notice of Allowance/Issue Fee dated _____

☒ Prior to a first examination on the merits

The changes and/or modifications made include the following:

A replacement Figure 1 is presented which properly labels the "Frequency Divider (1/N)" as element 2, to correct an informality in original Figure 1.

Respectfully Submitted,

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